

SAGA registers



Overview

The SAGA Core has dedicated registers in the following ranges:

- DE000x
- DFF1Ex
- DFF1Fx
- DFF3xx
- DFF4xx to DFF7xx

These registers are mainly used by the SAGA Drivers (RTG and SD drivers) and some other tools.

SAGA-specific registers

Below is the complete SAGA registers list:

Register	Name	Access	Size	Description
DE0000	saga_sdcard_data	RW	UBYTE	Read and Write Data
DE0004	saga_sdcard_ctl	W	UWORD	Write Control
DE0006	saga_sdcard_stat	R	UWORD	Read Status
DE000C	saga_sdcard_clk	W	UBYTE	Clock Speed
DE0008	saga_clock_counter	R	ULONG	Read the number of cycles since last call
DFF1EC	saga_video_planepr	W	ULONG	Set the SAGA Video FrameBuffer Address
DFF1F4	saga_video_mode	W	UWORD	Set the SAGA Video Mode
DFF1F8	saga_video_pll	W	UWORD	Set the SAGA ModeLine PIXELCLOCK
DFF1FA	saga_video_pll	R	UWORD	Set the SAGA ModeLine PIXELCLOCK
DFF300	saga_video_hpixel	W	UWORD	Set the SAGA ModeLine HPIXEL
DFF302	saga_video_hsstrt	W	UWORD	Set the SAGA ModeLine HSSTRT
DFF304	saga_video_hsstop	W	UWORD	Set the SAGA ModeLine HSSTOP
DFF306	saga_video_htotal	W	UWORD	Set the SAGA ModeLine HTOTAL
DFF308	saga_video_vpixel	W	UWORD	Set the SAGA ModeLine VPIXEL
DFF30A	saga_video_vsstrt	W	UWORD	Set the SAGA ModeLine VSSTRT
DFF30C	saga_video_vsstop	W	UWORD	Set the SAGA ModeLine VSSTOP
DFF30E	saga_video_vtotal	W	UWORD	Set the SAGA ModeLine VTOTAL
DFF310	saga_video_hvsync	W	UWORD	Set the SAGA ModeLine HVSYNC
DFF400	saga_video_clut [0]	W	ULONG	Set the SAGA CLUT8 Color #001

Register	Name	Access	Size	Description
DFF7FC	saga_video_clut [255]	W	ULONG	Set the SAGA CLUT8 Color #256
DFF1F8	saga_flash_write	W	UWORD	SPI Flash Write data
DFF1FA	saga_flash_read	R	UWORD	SPI Flash Read data

Remarks

Video PLL and **SPI Flash** are mapped to the same registers.

This Bus is shared between several users. Selection is done by sending a **Magic Word**.

OCS/ECS/AGA registers

For more information about the original Amiga chipset's registers:

- [Amiga Hardware Reference](#)
- [Amiga Hardware Manual Guide](#)

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