

# SAGA SPI Flash Core



## Overview

The SAGA SPI Flash Core provides an area in the FPGA to allow firmware updates from Operating System (AmigaOS 68k 3.x binary). This feature is also used to read the Revision of the installed Core and the Serial Number of the board/host.

See the [Installing new Cores](#) article for more practical information.

## Features

- **Flashing firmware** from Operating System.
- Reading the **Core Revision** of the firmware.
- Reading the **Serial Number** of the Vampire.
- **Detecting presence** of the Vampire board.

## FLASH registers

Register	Name	Access	Size	Description
DFF1F8	<a href="#">saga_flash_write</a>	W	UWORD	SPI Flash Write data
DFF1FA	<a href="#">saga_flash_read</a>	R	UWORD	SPI Flash Read data

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